

**IN THE SPECIFICATION**

Please replace the paragraph beginning on page 5, paragraph 0033 with the following paragraph, marked up to show changes made relative to the immediate prior version submitted:

[0033] In order to save chip space, several (in particular all) n-diffusion resistors 3a, 3b are arranged – side by side – in one single wn-well 2 (i.e. in addition to the first and second n-diffusion resistors 3a, 3b illustrated in Figure 21, several further, not illustrated n-diffusion resistors).